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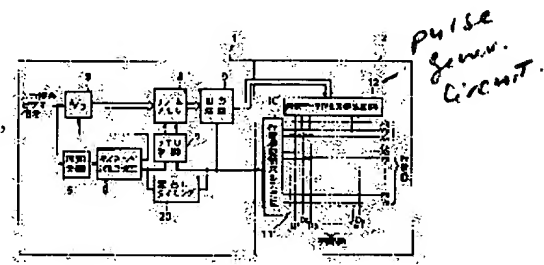
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## (54) PLASMA DISPLAY PANEL OF AC DISCHARGE TYPE MATRIX SYSTEM AND METHOD FOR DRIVING IT

### (57)Abstract:

PURPOSE: To drive a plasma display panel with a low power consumption by writing the pixel data while successively executing forced write discharge as preliminary discharge in order of arrangement of row electrode pairs.

CONSTITUTION: A row electrode drive pulse generation circuit 10' applies a positive polarity preliminary discharge pulse to a preliminary discharging row electrode Xp of the plasma display panel 11', and applies a negative polarity preliminary discharge pulse to the preliminary discharging row electrode Yp. Then, the row electrode drive pulse generation circuit 10' applies a positive polarity maintaining pulse to a row electrode X1, and applies a negative polarity forced write pulse to the row electrode Y1. Then, a pixel data pulse generation circuit 12 applies pixel data pulses corresponding to the pixel data of a first row to corresponding electrodes of column electrodes D1-Dn respectively. Together with that, the row electrode drive pulse generation circuit 10' applies the positive polarity maintenance pulse to the row electrode Y1. At this time, the forced write discharge by the forced write pulse is executed in order of the arrangement of the row electrode pairs successively.



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